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APPLICATION

MULTI-MODULATION RECEPTION METHOD BASED ON  
DEMODULATION OF SIGNALS FROM MODULATIONS WHOSE  
SYMBOLS ARE INCLUDED IN A MAIN CONSTELLATION

Fields of the invention

The field of the invention is signal processing applied to reception of signals and particularly radiocommunication signals.

More precisely, the invention relates to a method for receiving signals output from modulations for which the symbols are included in a set of symbols in  
5 a main constellation.

Solutions according to prior art

The conventional reception technique used by receiving terminals that have to demodulate several signals output from different symbol constellations,  
10 has always consisted in installing one detector in each receiver for each different modulation to be processed.

Disadvantages of prior art

A first disadvantage of this state of prior art relates to the increase in the  
15 complexity of the terminal, particularly when used for integration of different detectors. Integration of such a plurality of detectors inside the receiving terminal necessarily involves an increase in the size of the terminal, and this increase is contrary to ergonomic and / or miniaturisation constraints of radio-communication terminals, for example of the mobile telephone type.

20 Another disadvantage of this state of prior art relates to the importance of design costs induced by such an increase in the complexity of the receiving terminal, and also the importance of costs and / or extra costs, associated with the corresponding additional induced tests and validation, and extra costs related to production. But competition on the radiocommunications market at the moment is  
25 such that even small savings in the design and / or manufacturing of terminals are sufficient to reduce the final selling price and increase market shares.

### Objectives of the invention

The objective of the invention is particularly to overcome these disadvantages according to prior art.

5 More precisely, one purpose of the invention is to provide a signal processing method that can be used in any receiver, to make it capable of demodulating signals output from other modulations included in a main modulation.

10 Another purpose of the invention is to implement such a method to make the signal receiver independent of the modulation to be processed and therefore to avoid increasing the number of detectors within the receiver.

15 Another purpose of the invention is to provide such a method for reusing the detector of a main constellation of symbols contained in a receiver, to demodulate the signals of the modulations included in the main constellation, the receiver then being a multi-modulation receiver.

### Essential characteristics of the invention

20 These objectives and others that will become clear later, are achieved by means of a method for reception of a signal modulated according to a main constellation, called the main signal, and at least one signal modulated according to a secondary constellation, called the secondary signal. The secondary constellation is included in the main constellation. The method comprises a demodulation step of the main signal, outputting an information of confidence for each of the elements in the main constellation, related to reception of each element, called the main confidence information.

25 According to the invention, such a method advantageously comprises a step to determine at least one information of confidence related to reception of one element of the secondary constellation, called secondary information of confidence, using at least one of the main information of confidence so as to demodulate the secondary signal.

Thus, the invention is based on a new and inventive approach to demodulation of signals output from different modulations, but for which the symbols are included in a set of symbols of a main constellation.

Preferably, the element is one of the bits transmitted by a symbol in the  
5 main and / or secondary constellation.

Advantageously, in a second embodiment of the method according to the invention, the main confidence bit (or information of confidence) is a hard decision reception of the bit in the main signal. This hard decision is output from a hard outputs (also called hard decisions) detector that does not output soft bits  
10 directly.

The method according to the invention preferably comprises a prior step to determine the log likelihood ratio (LLR) of the bit called "soft bit", for at least some of the bits of the main signal, using the associated hard decision.

Preferably, the prior determination step uses a criterion belonging to the  
15 group comprising:

- the Lop-Map criterion;
- the Max-Log-Map criterion;
- the SOVA (Soft-Output Viterbi Algorithm based on the maximum likelihood criterion for detection of the most probable sequence).

20 It can also non-restrictively use an approximation of one of these criteria.

Advantageously, the main and / or secondary confidence bit associated with a bit is a log likelihood ratio (LLR) of the bit, called the main and / or secondary soft bit.

Also advantageously, the step to determine the secondary confidence bit  
25 comprises the following sub-steps:

- the secondary "soft bits" are expressed as a function of a posteriori probabilities of symbols in the secondary constellation, the symbols in the secondary constellation also belonging to the main constellation, so as to obtain a first expression;
- 30 - the a posteriori probabilities of bits in the main constellation are expressed as a function of the a posteriori probabilities of symbols in the main

constellation, bringing out the soft bits in the main constellation, output during the demodulation step of the main signal so as to obtain a second expression.

The method according to the invention also preferably comprises a sub-step for mathematical simplification of the first expression, using a saturated  
5 linear approximation or a piecewise linear approximation.

Advantageously, the method according to the invention also comprises a sub-step to classify symbols in the main constellation so as to minimise the number of soft bits in the main constellation used during the calculation of soft bits in the secondary constellation. Such a sub-step can optimise the calculation of  
10 the expression (4) described below for the first embodiment of the invention, by maximising the number of  $\alpha_i^n$  values equal to zero.

In one variant of the method according to the invention, the element is advantageously a symbol in the main and / or secondary constellation.

Preferably, the main and / or secondary confidence bit associated with a  
15 symbol is an a posteriori probability of a symbol in said main and / or secondary constellation.

During the main signal demodulation step, the main confidence bits are preferably calculated using one of the detection algorithms belonging to the group comprising:

- 20 - the Max-Log-Map;
- the Log-Map;
- SOVA (Soft-Output Viterbi Algorithm based on the maximum likelihood criterion for detection of the most probable sequence);
- DDFSE (Delayed Decision Feedback Sequence Estimation);
- 25 - RSSE (Reduced-State Sequence Estimation);
- M-algorithm;
- T-algorithm.

Advantageously, since the detection algorithm is two-directional, the secondary confidence bits associated with the symbols in the secondary  
30 constellation are secondary soft bits corresponding to the log likelihood ratio (LLR) values of symbol bits, and are determined by the following sub-steps:

- select a sub-set of a posteriori probabilities of symbols in the secondary constellation among the set of a posteriori probabilities of available symbols in the main constellation;

- determine secondary soft bits as a function of the sub-set of a posteriori probabilities of symbols in the secondary constellation, the symbols in the secondary constellation also belonging to the main constellation.

Furthermore, the prior determination sub-step uses a criterion preferably belonging to the following group:

- the Log-Map criterion;
- the Max-Log-Map criterion;
- SOVA (Soft-Output Viterbi Algorithm based on the maximum likelihood criterion for detection of the most probable sequence).

An approximation of one of these criteria can also be used in this determination sub-step.

Since the detection algorithm is single-directional, secondary confidence bits associated with symbols in the secondary constellation are advantageously secondary soft bits corresponding to the log likelihood ratio (LLR) values of symbol bits and are determined by the following sub-steps:

- select a sub-set of a posteriori probabilities of symbols in the secondary constellation among the set of a posteriori probabilities of available symbols in the main constellation;

- determine secondary soft bits as a function of the sub-set of a posteriori probabilities of symbols in the secondary constellation, the symbols in the secondary constellation also belonging to the main constellation;

- determine the sign of secondary soft bits as a function of the value of bits of symbols in the main constellation.

Preferably, the main and / or secondary constellations belong to the group comprising:

- M-QAM modulations, where  $M=2^m$
- N-PSK modulations, where  $N=2^n$  (particularly QPSK and BPSK);
- the linearised GMSK or MSK modulation.

The invention also advantageously relates to a receiving terminal of one modulated signal according to a main constellation, called the main signal, and at least one modulated signal according to a secondary constellation, called the secondary signal. The secondary constellation is included in the main constellation and the receiver comprising means of demodulating the main signal outputting a confidence bit related to reception of each element in the main constellation, called the main confidence bit. Such a receiver also advantageously comprises means of determining at least one confidence bit related to reception of at least one element in the secondary constellation, called the secondary confidence bit, using at least one of the main confidence bits, so as to demodulate the secondary signal.

Such a receiver advantageously uses a method for receiving a signal according to the invention, this signal being modulated according to a main constellation, and called the main signal, and for receiving at least one signal modulated according to a secondary constellation and called the secondary signal.

#### List of figures

Other characteristics and advantages of the invention will become clearer after reading the following description of a preferred embodiment given as a simple illustrative and non-limitative example and the attached drawings, among which:

- figures 1.a and 1.b show a block diagram of the method according to the invention, either by recombination of soft bits (figure 1.a) or by recombination of a posteriori probabilities (figure 1.b);
- figure 2 gives an illustration of the constellation associated with an 8-PSK modulation used in an EDGE system;
- figure 3 gives an illustration of the binary signal coding of the 8PSK EDGE as the main modulation and of a QPSK sub-constellation.

### Description of three embodiments of the invention

The general principle of the invention is based on demodulation of signals output from modulations in which symbols are included in a main constellation, by reusing a detector for which the previous initial function was solely to  
5 demodulate signals output from the modulation of all symbols in the main constellation.

Three embodiments of the method according to the invention are used to perform such processing, as presented in the remainder of this document.

A first embodiment of the method according to the invention consists of  
10 recombining confidence bits (soft bits) that can be in the form of log likelihood ratios (LLR), calculated on bits of symbols output from detection of the main modulation to deduce the soft bits of a sub-constellation. Depending on the case, such a recombination step of soft bits may or may not generate the loss of information, or may require prior simplification in order to facilitate  
15 implementation of the calculation of soft bits of the sub-constellation.

This first embodiment of the method according to the invention based on the recombination of soft bits is exemplified through the use of a GSM/GPRS/EDGE type receiver that uses an 8-PSK (8-Phase Shift Keying) type detector to demodulate GMSK (Gaussian Minimum Shift Keying) type signals  
20 without approximation and for which the complexity then becomes very low.

Remember the meaning of the acronyms mentioned above, which will be repeated throughout the remainder of the description.

The EDGE (Enhanced Data rate through GSM Evolution) system based on the 8-PSK modulation is the system replacing the GSM (Global System for  
25 Mobile Communication) system based on GMSK modulation.

In a second embodiment of the invention, applied to the use of hard decision detectors (or outputs), steps to reconstruct the soft bits are introduced and enrich steps used in the first proposed embodiment of the invention.

The third proposed embodiment of the invention relates to the use of  
30 reprogrammable generic detectors for which the method no longer operates on soft bits but on a posteriori probabilities (APP) of symbols in the main



constellation. Two generic architectures, one adapted to single-directional algorithms, and the other to two-directional algorithms, can be used for the second embodiment of the method according to the invention. They are described in section 7.3 in this document.

5           We will describe three embodiments of the invention with reference to figures 1.a, 1.b, 2 and 3.

Description of a first embodiment of the invention by recombination of soft bits of a detector with soft outputs

10           In this first embodiment, the method according to the invention is applicable to the case in which the receiver comprises a detector with soft outputs with the function of demodulating a signal belonging to a first main constellation and that must be reused without any modification to detect a signal (that we will subsequently call a secondary signal) belonging to a sub-constellation of the main  
15 constellation. The main constellation contains a predetermined set of a finite number of symbols ( $N > 0$ ), and therefore the sub-constellation contains a predetermined sub-set of a finite number of symbols ( $n \leq N$ ).

          In this first embodiment, the method according to the invention is described in the case of demodulation of a secondary signal output from a  
20 modulation for which symbols belong to a sub-constellation of a main constellation. However, the method described may easily be generalised to the case of detection of several secondary signals belonging to several sub-constellations of the main constellation, by reuse of the detector initially designed to demodulate the signal from the main constellation.

25           We use the figure 1.a to present and describe the method according to the first embodiment of the invention, and in this case we consider emission (1) of two modulation constellations M0 (2) and M1 (3) such that M1 (3) (sub-constellation) is included in M0 (2) (main constellation). The number of bits transmitted per symbol in modulation M0 will then be called  $m_0$  and the number  
30 of bits transmitted per symbol in modulation M1 will be called  $m_0$ .

The main constellation M0 then contains  $2^{m_0}$  symbols and sub-constellation M1 contains  $2^{m_1}$  symbols, where ( $m_1 < m_0$ ).

A so-called confidence bit is used to demodulate the signal output from modulation M1. In this case it corresponds to a log likelihood ratio (LLR) or an approximation of this logarithm, in other words a logarithm of the probability that a bit (that can only be equal to two values, either 0 or 1) of a symbol emitted from M0 actually corresponds to 0 or 1 when it is received. This confidence bit is calculated in the form of log likelihood ratio (LLR) called the main and / or secondary soft bit, for each of the bits in symbols in M0 and / or M1.

In this first embodiment, the method according to the invention is broken down in the following three main steps necessary to determine the confidence bit related to the value of bits received through the transmission channel (4) for symbols belonging to sub-constellation M1 (3) (secondary soft bits) using the soft bits associated with symbols in the main modulation (2);

- Step 1: secondary soft bits are expressed as a function of a posteriori probabilities of symbols in secondary constellation M1 (also belonging to the main constellation M0) so as to obtain a first expression giving a recursive formulation of the LLR of the secondary modulation M1 as a function of the LLR values of the main modulation M0;

- Step 2: if necessary, the expression obtained in step 1 is mathematically simplified using an approximation in the form of a function  $f(\cdot)$  written in the form  $f(x)=\log(1+e^x)$ ;

- Step 3: the a posteriori probabilities of detected bits (8) in the main constellation M0 are expressed as a function of a posteriori probabilities of symbols in the main constellation M0, including soft bits of the main constellation that are output in the preliminary demodulation step (5) of the main signal.

These three steps are described in more detail below.

#### Step 1:

This step consists of writing log likelihood ratios (LLR), in other words soft bits for each bit of symbols belonging to a sub-constellation M1.

The LLR of  $m_0$  bits associated with the main modulation M0, assumed to be known and calculated by the detector that is required to be reused here, are denoted  $X_0, \dots, X_{m_0-1}$ .

Similarly, the LLR values of  $m_1$  bits associated with the secondary modulation M1 that is to be expressed as a function of bits  $X_0, \dots, X_{m_0-1}$  calculated by the detector and assumed to be known, are denoted  $Y_0, \dots, Y_{m_1-1}$ .

$E_k$  denotes all indexes of symbols in sub-constellation M1 for which the bit with index  $k$  that depends on the binary signal coding, is equal to 0 and its complement in the main constellation M0 is denoted  $C_{M_0}(E_k)$ .

Note also that conventionally, the a posteriori probability of a symbol  $S_i$  is denoted  $P(S_i)$  and the LLR of the bit with index  $k$  associated with the secondary modulation M1 is denoted as follows:

$$Y_k = \log \frac{\sum_{i \in E_k} P(S_i)}{\sum_{i \in C_{M_1}(E_k)} P(S_i)} \quad (1)$$

15

It then becomes possible to reindex the symbols such that the indexes varying from 0 to  $\frac{M_1}{2} - 1$  are associated with the symbols for which the bit with index  $k$  is equal to 0 and indexes from  $\frac{M_1}{2}$  to  $(M_1 - 1)$  are associated with symbols for which the bit with index  $k$  is equal to 1. Therefore expression (1) can be rewritten as follows:

20

$$Y_k = \log \frac{\sum_{i=0}^{M_1/2-1} P(S_i)}{\sum_{i=M_1/2}^{M_1-1} P(S_i)} \quad (2)$$

Step 2:

This step consists of simplifying writing of equation (2) by introducing the above mentioned function  $f(x) = \log(1+e^x)$ .

Therefore, after the function  $f(.)$  has been introduced, expression (2) can be written in the form of a recursive formulation of the LLR of the secondary modulation M1 as a function of the LLR values of the main modulation M0:

$$Y_k = \log \frac{P(S_0)}{P(S_{M_1/2})} + f\left(\log \frac{P(S_1)}{P(S_0)}\right) + f\left(\log \frac{P(S_2)}{P(S_1)}\right) \dots + f\left(\log \frac{P(S_{M_1/2-1})}{P(S_{M_1/2-2})}\right) \dots) \\ - f\left(\log \frac{P(S_{M_1/2+1})}{P(S_{M_1/2})}\right) \dots + f\left(\log \frac{P(S_{M_1/2+2})}{P(S_{M_1/2+1})}\right) \dots + f\left(\log \frac{P(S_{M_1})}{P(S_{M_1-1})}\right) \dots) \quad (3)$$

Thus, as an illustrative example, assuming that the number  $m_1$  of bits associated with the secondary modulation M1 that is to be expressed as a function of the bits of symbols in the main constellation, is equal to 3, the result is an expression of  $Y_K$  written as follows:

$$Y_k = \log \frac{P(S_0) + P(S_1) + P(S_2) + P(S_3)}{P(S_4) + P(S_5) + P(S_6) + P(S_7)}$$

which can be written as follows based on the simplification obtained by introduction of the function  $f(.)$ :

$$Y_k = \log \frac{P(S_0)}{P(S_4)} + f\left(\log \frac{P(S_1)}{P(S_0)}\right) + f\left(\log \frac{P(S_2)}{P(S_1)}\right) + f\left(\log \frac{P(S_3)}{P(S_2)}\right) - f\left(\log \frac{P(S_5)}{P(S_4)}\right) + f\left(\log \frac{P(S_6)}{P(S_5)}\right) + f\left(\log \frac{P(S_7)}{P(S_6)}\right)$$

Depending on the case and the required degree of simplification, the function  $f(.)$  used will be either tabulated or simplified. For a simplified function, the function  $f(.)$  may non-limitatively be either a saturated linear type function or a piecewise linear function.

In the case of a saturated linear function, the function  $f(.)$  is then written in the following conditional form:

$$\begin{aligned} f(x) &= x, & \text{if } x > 0 \\ f(x) &= 0 & \text{otherwise.} \end{aligned}$$

5 In the case of a piecewise linear function, the function  $f(\cdot)$  corresponds to a more complex shaped approximation, that has the advantage that it can use a straight line for which the slope is determined by a division by two, in other words all bits are offset by one towards the right. It is then written in the following form:

$$\begin{aligned} S &= \log(2) / 2 \\ f(x) &= x & \text{if } x > S \\ f(x) &= \frac{x}{2} + \log(2) & \text{if } -S \leq x \leq S \\ f(x) &= 0 & \text{if } x < -S \end{aligned}$$

10

### Step 3:

Thus, since bits of symbols are transmitted independently, it becomes possible to write the LLRs between symbols present in expression (3) above including the values  $X_i$  of the corresponding LLR values of the  $m_0$  bits associated with the main modulation M0, which are actually calculated by the receiver detector, one essential role of which is normally to process only symbols output from a single predetermined main constellation.

15

Therefore each LLR between symbols present in expression (3) can be written in the following developed form:

20

$$P(S_i) = \prod_{k=0}^{m_0-1} P(b_k(S_i))$$

where  $b_k(S_i)$  corresponds to the bit with index  $k$  in symbol  $S_i$  belonging to the set of symbols in the main constellation M0.

25

This developed expression of each of the soft bits (LLR) associated with symbols (secondary symbols) in the sub-constellation M1 also contained in the set

of symbols in the main constellation M0, is used to include values of  $X_i$  (for index  $i$  varying from 0 to  $m_0-1$ ) actually calculated by the detector (6), in the expression of the secondary soft bits (7). Each of the terms in the logarithm included in expression (3) and used to calculate  $Y_k$  values, where  $k$  is between 0 and  $m_1-1$ ), can then be replaced as follows:

$$\log \frac{P(S_{n+1})}{P(S_n)} = \sum_{i=0}^{m_0-1} \alpha_i^n X_i \quad (4)$$

where  $\alpha_i^n \in \{-1, 0, 1\}$

In one implementation phase of the method according to the invention in a receiving terminal that is based on this first described embodiment, it is important to emphasize that a further simplification could be made simply by judicious choices of the expressions.

In particular, a sub-step will be introduced to minimise the Hamming distance between symbols in the main constellation, to simplify the formulation and calculation of the expression (4).

Indeed, formula (3) is not unique and its calculation depends particularly on the manner in which the different symbols in the main constellation are indexed. It is then possible to put symbols into order such that the number of terms  $\alpha_i$  not equal to zero in formula (4) is minimised, which is therefore equivalent to minimising the Hamming distance between each symbol  $S_i$  and symbols  $S_{i+1}$ .

When the bits transmitted by the symbols in sub-constellation M1 form a part of the bits of the symbols in the main constellation M0, at least one value  $\alpha_i$  in expression (4) is equal to zero.

Therefore all that is necessary to further minimise the number of values  $\alpha_i = 0$  is to put the symbols into order so as to minimise the Hamming distance between each symbol and its neighbours after it and before it.

Consider the example in which  $m_0 = 4$  (where  $m_0$  is equal to the number of bits transmitted per symbol in modulation M0) and  $m_1 = 3$  (where  $m_1$  is equal to the number of bits transmitted per symbol in modulation M1). Therefore, symbols with indexes  $S_0, S_1, S_2, S_3$  have at least one bit in common, which means that their Hamming distance is less than  $m_0 - 1 = 3$ . In this case, there are four symbols all coded on 4 bits, and all having a fixed bit in common.

There are then three available freedom bits that are then used in calculations to put the symbols into more optimum order.

This simplification technique based on the calculation of the Hamming distance between symbols is useful for designing binary signal coding, specifying firstly the emission and secondly minimising the complexity of the receiver re-using the main modulation detector.

We will now present simple illustrative and non-limitative examples of two cases using the first embodiment of the method according to the invention.

#### Example No. 1

Consider the example in which  $m_1 = 1$  ( $m_1$  is the number of bits transmitted per symbol in modulation M1), therefore M1 is a sub-constellation with two states which can therefore be written directly in the form of a combination of  $X_i$ , in other words soft bits (LLR) of symbols in the main constellation. Therefore, in this case there is no need to perform the simplification step mentioned above using the function  $f(\cdot)$ .

This example corresponds to a description of the constellations in the case of GMSK / 8-PSK modulations.

The GMSK modulation in the GSM / GPRS / EDGE standard may be approximated by a BPSK modulation with a  $\pi/2$  offset filtered by the EDGE emission filter. Once the offset has been eliminated by deoffseting, the possible emitted symbols are then equal to the values  $+1 / -1$ .

As shown in figure 2, the two symbols  $+1 / -1$  will also be elements of the alphabet (21, 22) of the 8-PSK modulation used in the EDGE system.

All that is necessary to calculate the confidence bit in the form of log likelihood ratios (LLR) of GMSK symbols at the output from a 8-PSK detector is to write LLR values associated with the GMSK modulation symbols as follows:

$$5 \quad Y_0 = \log \frac{P(+1)}{P(-1)} = \log \frac{P_0(1)P_1(1)P_2(1)}{P_0(0)P_1(0)P_2(1)} = \log \left( \frac{P_0(1)}{P_0(0)} \right) + \log \left( \frac{P_1(1)}{P_1(0)} \right)$$

where

- $P(+1)$  is the a posteriori probability (APP) of the +1 symbol;
- $P(-1)$  is the a posteriori probability (APP) of the -1 symbol;
- 10 -  $P_i(1)$  is the a posteriori probability (APP) that bit  $i$ , where  $0 \leq i \leq m_0$ , is equal to 1;
- $P_i(0)$  is the a posteriori probability (APP) that bit  $i$ , where  $0 \leq i \leq m_0$ , is equal to 0.

We can then deduce that  $Y_0 = -(X_0 + X_1)$ .

15 Therefore this means that with no approximation, the soft bit of the GMSK modulation is written as the sum of soft bits with index  $(3k)$  and  $(3k+1)$  of the 8-PSK modulation, with changed sign.

Therefore the method to recombine soft bits is particularly simple when it is applied to the linearised GMSK modulation. Furthermore, it does not make any  
20 assumption about the manner in which soft bits are calculated by the 8-PSK detector which makes it generic and independent.

Example No. 2: Case of an OPSK type secondary modulation transmitted on an EDGE type main constellation.

25 This new example shown in figure 3 shows the most complex case of an EDGE type 8 PSK main modulation (31) and a QPSK (quadrature Phase Shift keying) type secondary modulation 32 and a GMSK type secondary modulation 33. The corresponding binary signal coding of the symbols are given and are listed in the corresponding table given below:



Symbol	Binary code
$S_0$	(1, 1, 1)
$S_1$	(0, 1, 1)
$S_2$	(0, 1, 0)
$S_3$	(0, 0, 0)
$S_4$	(0, 0, 1)
$S_5$	(1, 0, 1)
$S_6$	(1, 0, 0)
$S_7$	(1, 1, 0)

Expression (3) mentioned above is used to write the LLR values  $Y_0$  and  $Y_1$  for  $m_1 = 2$ bits associated with the QPSK secondary modulation 32 as follows:

$$5 \quad Y_0 = \log \frac{P(S_2) + P(S_4)}{P(S_0) + P(S_6)} = \log \frac{P(S_2)}{P(S_0)} + \log(1 + \frac{P(S_4)}{P(S_2)}) - \log(1 + \frac{P(S_6)}{P(S_0)})$$

and

$$Y_1 = \log \frac{P(S_4) + P(S_6)}{P(S_0) + P(S_2)} = \log \frac{P(S_4)}{P(S_0)} + \log(1 + \frac{P(S_6)}{P(S_4)}) - \log(1 + \frac{P(S_2)}{P(S_0)})$$

The result after simplification by function  $f(\cdot)$  is as follows:

10

$$Y_0 = X_0 + X_2 + f(X_1 - X_2) - f(X_1 + X_2)$$

$$Y_1 = X_0 + X_1 + f(-X_0 + X_2) - f(X_0 + X_2)$$

15 The next step is to apply one of the above-mentioned methods to simplify the expressions of  $Y_0$  and  $Y_1$  obtained. The  $f(\cdot)$  function used may then be either in the form of a saturated linear type function or a piecewise linear function.

Simplification number 1: application of the saturated linear function  $f(\cdot)$ .

In the case of a saturated linear function, the first step to calculate the first soft bit  $Y_0$  associated with the QPSK secondary modulation consists of calculating the following quantities:

$$\begin{aligned} S_0 &= X_1 - X_2 \\ S_1 &= X_1 + X_2 \end{aligned}$$

Depending on the result of the comparisons of  $S_0$  and  $S_1$  with 0, the secondary soft bit  $Y_0$  of the QPSK modulation is then expressed as being one of the relations given in the following table, which expresses the value of the soft bit  $Y_0$  as a function of the "soft bits" calculated by the 8PSK main modulation detector:

	$S_0 \leq 0$	$S_0 > 0$
$S_1 \leq 0$	$Y_0 = X_0 + X_2$	$Y_0 = X_0 + X_1$
$S_1 > 0$	$Y_0 = X_0 - X_1$	$Y_0 = X_0 - X_2$

Similarly, the following quantities are calculated so that the second soft bit  $Y_1$  associated with the QPSK secondary modulation can be calculated:

$$\begin{aligned} S_2 &= X_2 - X_0 \\ S_3 &= X_2 + X_0 \end{aligned}$$

20

Depending on the result of the comparisons of  $S_2$  and  $S_3$  with 0, the secondary soft bit  $Y_1$  of the QPSK modulation is then expressed as being one of the relations given in the following table, which expresses the value of the soft bit  $Y_1$  as a function of the soft bits calculated by the 8PSK main modulation detector:

	$S_2 \leq 0$	$S_2 > 0$
$S_3 \leq 0$	$Y_1 = X_0 + X_1$	$Y_1 = X_1 + X_2$
$S_3 > 0$	$Y_1 = X_1 - X_2$	$Y_1 = X_1 - X_0$

Simplification No. 2: application of the most complex approximation  $f(.)$  called piecewise linear approximation (saturated).

If a piecewise linear function (saturated) is applied, the first step also  
5 consists in calculating the four sums  $S_0$ ,  $S_1$ ,  $S_2$  and  $S_3$ .

The next step is then to compare the four sums with the  $+S$  and  $-S$  thresholds calculated as follows:  $S = \frac{\log(2)}{2}$ .

In this case, the corresponding values of the QPSK secondary modulation bits  $Y_0$  and  $Y_1$  are then deduced, as summarised in the following table for the soft  
10 bit  $Y_0$  expressed as a function of soft bits  $X_i$  actually calculated by the 8PSK detector:

	$S_0 < -S$	$-S \leq S_0 \leq S$	$S_0 > S$
$S_1 < -S$	$Y_0 = X_0 + X_2$	$Y_0 = \frac{X_2 + X_1}{2} + X_0 + 2S$	$Y_0 = X_0 + X_1$
$-S \leq S_1 \leq S$	$Y_0 = \frac{X_2 - X_1}{2} + X_0 - 2S$	$Y_0 = X_0$	$Y_0 = \frac{-X_2 + X_1}{2} + X_0 - 2S$
$S_1 > S$	$Y_0 = X_0 - X_1$	$Y_0 = \frac{-X_2 - X_1}{2} + X_0 + 2S$	$Y_0 = X_0 - X_2$

Description of a second embodiment of the invention: case of detectors with hard outputs

15 In the second embodiment of the invention, the method in the first embodiment is enriched by performing two new steps before the above mentioned three steps related to the first embodiment of the invention, so as to provide it with the capability of extracting and using soft bits from the bits of a sub-constellation, even if the detector used for the main modulation only outputs hard bits.

20 These two new steps shown in figure 1.b are characteristics of the second embodiment of the method according to the invention. They concern the following respectively:

- reconstruction (6) of the soft bits of the main modulation using a Log-Map type criterion or a Max-Log-Map type criterion;

- recombination of the reconstructed soft bits of M0 (9) to obtain soft bits of the sub-constellation M1 (10).

5 We will now describe the step to reconstruct soft bits of the main modulation.

The detector used only outputs hard bits (or symbols) denoted  $S$  and composed of a number  $m_0$  of bits  $b_i(S)$  for  $0 \leq i < m_0$ . Starting from hard decisions and the binary signal code of the main constellation assumed to be  
10 known, it then becomes possible to reconstitute a soft bit  $X_k$  for each of the  $m_0$  bits  $b_i(S)$  of the hard symbol  $S$ . This reconstruction of each soft bit  $X_k$  in the main constellation uses the Max-Log-Map type criterion and then consists of the following additional steps:

a) search for the symbol in the main constellation that minimises the  
15 distance from the hard symbol  $S$  and with a bit at position  $k$  characterised in that it is the complement of  $b_k(S)$ ;

b) calculate the distance between the symbol selected in step a) and the hard symbol  $S$ ;

c) assign a positive or a negative sign to the distance calculated in the step  
20 b) as a function of the value of  $b_k(S)$ .

It is important to note that this reconstruction function is implemented very simply using a table containing  $m_0$  distances for each symbol in the main constellation, related to the  $2^{m_0}$  bits of the symbols in this main constellation. This table is directly addressed by the value of the hard symbol  $S$ . Therefore it  
25 comprises  $m_0 * 2^{m_0}$  elements.

The Log-Map criterion can also be used in the preliminary reconstruction step of the soft bits. It is used in the same way as the Max-Log-Map criterion. The only difference in the use of one or the other of these two criteria is based on the fact that all symbols in the main constellation, for which the position bit  $k$  is the

complement of  $b_k(S)$  of the hard symbol  $S$ , are used in the calculation of  $X_k$ , in other words in the calculation of the reconstructed soft bits of the main constellation. Implementation of this second method based on the Log-Map-Criterion then also uses a table comprising  $m_0 * 2^{m_0}$  elements, which is addressed  
 5 in the same way as for use of the Max-Log-Map criterion.

The purpose of the second preliminary step characteristic of this second embodiment of the method according to the invention is to enable recombination of the soft bits of the main constellation so as to determine the soft bits  $Y_k$  of a sub-modulation.

10 This recombination step is based particularly on the following successive steps 1 to 3 identical to the steps described above in the section related to the first embodiment of the method according to the invention:

- Step 1: the secondary soft bits are expressed as a function of a posteriori probabilities of symbols in the secondary constellation M1 (also belonging to the  
 15 main constellation M0) so as to obtain a first expression giving a recursive formulation of the LLR of the secondary modulation M1 as a function of the LLR values of the main modulation M0;

- Step 2: the expression obtained in step 1 is mathematically simplified if necessary using an approximation in the form of a function  $f(\cdot)$  written in the form  
 20  $f(x) = \log(1 + e^x)$ ;

- Step 3: the a posteriori probabilities of bits in the main constellation M0 are expressed as a function of the a posteriori probabilities of symbols in the main constellation M0, including the soft bits in the main constellation that are output from the reconstruction of soft bits of the main modulation using a Log-Map type  
 25 criterion or a Max-Log-Map type criterion, using the hard outputs of the detector used.

Furthermore, due to the simple fact that the values obtained for the bits of the symbols in the main constellation  $X_k$  are deterministic, it also is possible to tabulate recombination operations to optimise the method.

Description of a third embodiment of the invention: case of configurable generic detectors

In the first two embodiments of the method according to the invention described above, the proposed technique is based on two main steps. The first step  
 5 consists of using soft bits output from the main modulation detector (either directly in the case of a soft outputs detector, or by prior reconstruction in the case of a hard outputs detector). The second step then consists of calculating the soft bits associated with the symbols in a sub-constellation included in the main constellation, using the results of the first step.

10 In this third embodiment of the invention, the method no longer uses soft bits output from the detector as confidence bits, and instead uses a posteriori probabilities (APP) of symbols in the main constellation.

If a reconfigurable generic detector is used, it is easy to obtain a table of  $m_0$  a posteriori probabilities (APP) for each symbol in the main constellation to be  
 15 detected, calculated using the following algorithms:

- Log-Map;
- Max-Log-Map;
- SOVA (Soft-Output Viterbi Algorithm) based on the maximum likelihood criterion for detection of the most probable sequence);
- 20 - DDFSE (Delayed Decision Feedback Sequence Estimation);
- RSSE (Reduced-State Sequence Estimation);
- M-algorithm;
- T-algorithm.

The next step is to calculate the soft bits of symbols in the order  $m_1$  sub-  
 25 constellation, depending on whether the algorithm is two-directional or single-directional.

Confidence bits for two-directional detection algorithms are calculated in the form of log likelihood ratios (LLR) of the sub-constellation, and this calculation then consists of the following steps as a function of the binary signal  
 30 coding of this sub-constellation:

- select a sub-set containing  $m_1$  a posteriori probabilities among the  $m_0$  available values;

- define the  $k$  sub-sets  $E_k$  of indexes of symbols in the sub-constellation (for which the index  $k$  that depends on the binary signal coding is equal to 0) and  
5  $C_{M_1}(E_k)$  (complement of  $E_k$  in the secondary constellation);

- apply the following relation to obtain the  $m_1$  soft bits  $Y_k$  in the form of an LLR:

$$Y_k = \log \frac{\sum_{i \in E_k} P(S_i)}{\sum_{i \in C_{M_1}(E_k)} P(S_i)} \quad (5)$$

10

For information it can be mentioned that this relation (5) is calculated indifferently, either using the Log-Map type criterion, or using a Max-Log-Map type criterion.

For single-directional detection algorithms, the sign of the soft bits of the  
15 main modulation is obtained by a trace backing operation. Such an operation can be used to obtain information maximizing the likelihood ratio of the received sequence.

Thus, to calculate the soft bits of the sub-constellation, the soft bits of the sub-constellation are calculated as a function of the binary signal coding of the  
20 sub-constellation using the following steps:

- select a sub-set containing  $m_1$  a posteriori probabilities among the available  $m_0$ ;

- define  $k$  sub-sets  $E_k$  (indexes of symbols in the sub-constellation for which the index bit  $k$  is equal to 0) and  $C_{M_1}(E_k)$  (complement of  $E_k$  in the main  
25 constellation);

- apply the following relation to obtain the  $m_1$  soft bits  $Y_k$  in the form of an LLR:

$$Y_k = \log \frac{\sum_{i \in E_k} P(S_i)}{\sum_{i \in C_{M_1}(E_k)} P(S_i)}$$

The next step is to determine the sign of  $Y_k$  using the step in the second embodiment mentioned above to reconstruct the soft bits of the main modulation  
 5 using hard decisions output by the detector used.

#### Summary of the three embodiments described

Three embodiments of the method according to the invention are described to demodulate signals included in a main modulation, using the main modulation  
 10 detector.

The two first embodiments combine soft bits, in other words confidence bits (soft bits) generated by the main detector and do not require any modification of the hardware part of the receiving terminal and / or the detector used.

The third embodiment results in a generic hardware architecture capable of  
 15 calculating soft bits of all secondary modulations of a main modulation.

For example, the method of recombining soft bits becomes particularly simple in the case of a GSM / GPRS / EDGE type receiver because it consists simply of summing two soft bits of the 8-PSK detector out of three, and then changing the sign of the result in order to obtain the soft bit associated with the  
 20 GMSK modulation. In practice, the technique described in the invention makes it possible to advantageously use reception algorithms designed for EDGE, with no additional development costs applying them to demodulation of GMSK signals.